http://www.ccs.tsukuba.ac.jp/

PACS-CS Next generation computational resource

PACS-CS (Parallel Array Computer System for Computational Sciences)

► PACS-CS is a specially designed PC cluster which will be the largest and fastest one in Japan after its completion on June 2006. It is designed for true high-performance on scientific calculations which require wide bandwidth both on memory and network. The concept of PACS-CS is "bandwidthaware massively parallel processing system based on commodity technology".

3-D Hyper-Crossbar Network

Special mother-board for

with trunked Gigabit Ethernet

We adopt 3-dimensional Hyper-Crossbar Network as the interconnection network to trunked Gigabit Ethernet links which realize scalable and high-bandwidth communication with low-cost NIC and switch. It provides a powerful network to support nearest neighboring communication as well as random communication, without I/O bus bottleneck. The mother-board is equipped with eight of Gigabit Ethernet interfaces without standard PCI slot form factor, which are driven by a dedicated driver for this special network.



single CPU/node

Each node consists of a single CPU which exploits the full bandwidth of memory to keep the performance even for non cache-aware applications. We are developing a special mother-board to realize the same density of implementation with traditional PC clusters with dual-CPU SMP.



Network Architecture (3-D Hyper-Crossbar)



19-inch 1U form chassis

PACS-CS Specifications

Number of nodes (CPUs)	2560 (16 × 16 × 10)
Peak performance	14.3 TFLOPS
CPU	Intel Low-Voltage Xeon EM64T 2.8GHz 1MB Cache
Memory size	2 GByte/node (5.12 TByte/system)
Memory bandwidth/CPU	6.4 GByte/sec (1.14 Byte/sec/FLOPS)
Local HDD	160 GByte × 2 (RAID-1 mirroring)
OS	Linux (Fedora Core) + Score
Programming	Fortran90, C, C++, MPI